



PATENT APPLICATION  
Docket No. 8750-017  
Client Ref. No. SPX2001100010US

IFW

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Sang-Eun Lee and Jae-Sung Han

Serial No. 10/068,152

Examiner: Hollington, Jermele M.

Conf No.: 7500

Filed: February 6, 2002

Art Unit: 2829

For: METHOD OF IDENTIFYING AND ANALYZING SEMICONDUCTOR  
CHIP DEFECTS

Mail Stop AMENDMENT  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Responsive to the Office Action dated June 3, 2004, enclosed is an amendment in the above-identified application.

The fee has been calculated as shown below.

CLAIMS AS AMENDED					
For:	Number After Amendment	Previous Number	Extra	Rate	Additional Fee
Total Claims	21	21	0	x \$18 =	\$0
Independent Claims	4	4	0	x \$84 =	\$0
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT					\$0

- ☒ A Copy of Hosoon Lee's Limited Recognition Under 37 CFR § 10.9(b).
- ☒ Return Receipt Postcard.
- ☒ Any deficiency or overpayment should be charged or credited to deposit account number 13-1703.

Customer No. 20575

Respectfully submitted,

MARGER JOHNSON & McCOLLOM, P.C.

Hosoon Lee  
Limited Recognition Under 37 CFR § 10.9(b)

MARGER JOHNSON & McCOLLOM, P.C.  
1030 SW Morrison Street  
Portland, OR 97205  
(503) 222-3613

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Mail Stop Amendment; Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Date: September 3, 2004

Angie C. Farr



PATENT APPLICATION  
Docket No. 8750-017  
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Serial No. 10/068,152 Examiner: Hollington, Jermele M

Confirmation No. 7500

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SEMICONDUCTOR CHIP DEFECTS

MAIL STOP AMENDMENT  
Commissioner for Patents  
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AMENDMENT

This paper is responsive to the Office Action, Paper No. 052804, mailed 3 June 2004.

**Amendments to the Claims** begin on page 2 of this paper.

**Remarks/Arguments** begin on page 6 of this paper.